



JBW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Re the Application of  
Anindya Saha, et al.  
Serial No.: 10/710,861  
Filed: August 9, 2004  
For: Providing Optimal Supply Voltage to Integrated Circuits

TI-36220  
Art Unit: 2816  
Examiner:  
Conf. No: 4860

**LETTER TO THE OFFICIAL DRAFTSPERSON**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

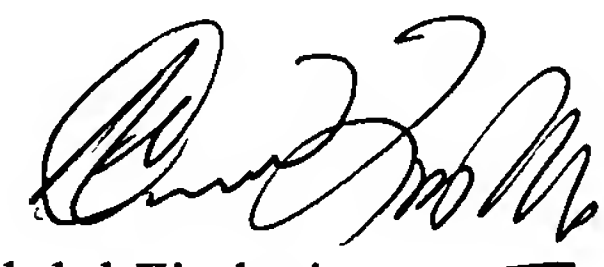
MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited  
with the U.S. Postal Service as First Class Mail in an envelope  
addressed to: Commissioner for Patents, P.O. Box 1450,  
Alexandria, VA 22313-1450 on November 17, 2004.

  
Robin E. Barnum

Sir:

Please find enclosed the formal drawings for the subject case. Charge any necessary fees to Texas Instruments Incorporated, Deposit Account No. 20-0668. **This form is submitted in triplicate.**

Respectfully submitted,

  
Abdul Zindani  
Attorney for Applicant  
Reg. No. 46,091

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